

**K.S. GROUP OF INSTITUTIONS, BENGALURU - 560 109**  
**K.S. SCHOOL OF ENGINEERING AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**ACADEMIC YEAR : 2019-20 (ODD SEMESTER)**  
**CLASS TIME TABLE**

CLASS : III ECE 'A'

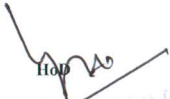
LECTURE HALL:310


DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50
MONDAY	PEI (CRG)	M3 (NTK)	TEA BREAK	PEI (CRG)	NT (SS)	LUNCH BREAK	DSDL (Batch-1/SBN,ZB) / EDL (Batch-2/CRG,SWT)		
TUESDAY	M3 (NTK)	DSD (SAP)		ED (DRB)	NT (SS)		DSD (SAP)	CPH (AMV)	CO (KM)
WEDNESDAY	NT (SS)	CO (KM)		M3 (NTK)	ED (DRB)		DSDL (Batch-2/SBN,ZB) / EDL (Batch3/SWT,DRB)		
THURSDAY	DSD (SAP)	CO (KM)		ED (DRB)	DSD (SAP)		M3 (NTK)	NT (SS)	PEI (CRG)
FRIDAY	CO (KM)	ED (DRB)		M3 (NTK)	PEI (CRG)		DSDL (Batch-3/SWT,ZB) / EDL (Batch-1/CRG,DRB)		
SATURDAY	AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

CODE	SUBJECT	HOURS /WEEK	STAFF
18MAT31	Transform Calculus, Fourier Series and Numerical Techniques (M3)	5	Mrs. Nagarathna T K (NTK)
18EC32	Network Theory(NT)	4	Mrs. Shalini Shravan (SS)
18EC33	Electronic Devices (ED)	4	Mrs. Deepa R Bhangri (DRB)
18EC34	Digital System Design(DSD)	4	Mrs. Shilpa A P (SAP)
18EC35	Computer organisation and architecture(CO)	4	Dr. Kishore M (KM)
18EC36	Power Electronics Instrumentation(PEI)	4	Mr. Gopal KrishnaMurthy C R (CRG)
18ECL37	Electronic devices & Instrumentation Laboratory(EDL)	3	Mr. Gopal KrishnaMurthy C R (CRG)/ Mr. Syed Waseem Tabraiz (SWT)/ Mrs. Deepa R Bhangri (DRB)
18ECL38	Digital System Design(DSD)	3	Mr. Sanjay B Nayak (SBN)/ Mr. Syed Waseem Tabraiz (SWT)/ Ms. Zaiba(ZA)
18CPC39/49	Constitution of India/Professional Ethics & Cyber Law(CPH)	1	Mrs. Anuradha M V (AMV)

Class Teacher
Mrs. Shalini Shravan
8197346348

  
Time Table Co-ordinator

  
Dept. of Electronics & Communication Engineering  
K. S. School of Engineering & Management  
Bangalore-560 109

  
Principal  
**Dr. K. RAMA NARASIMHA**  
Principal/Director  
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**ACADEMIC YEAR : 2019-20 (ODD SEMESTER)**  
**CLASS TIME TABLE**

CLASS : III ECE 'B'


LECTURE HALL:311

DAY	8.40-9.35	9.35-10.30	10.30-10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50
MONDAY	DSDL (Batch-1/SBN,SWT) / EDL (Batch-2/VR,DRB)				M3 (DR)	LUNCH BREAK	ED (VR)	CO (KM)	DSD (RVT)
TUESDAY	M3 (DR)	CPH (AMV)	TEA BREAK	PEI (CRG)	NT (PS)		DSD (Batch-2/SBN,ZB) / EDL (Batch-3/CRG,VR)		
WEDNESDAY	ED (VR)	NT (PS)		CO (KM)	PEI (CRG)		M3 (DR)	DSD (RVT)	ED (VR)
THURSDAY	NT (PS)	PEI (CRG)		DSD (RVT)	M3 (DR)		DSD (Batch-3/SBN,ZB) / EDL (Batch-1/VR,SWT)		
FRIDAY	DSD (RVT)	PEI (CRG)		M3 (DR)	CO (KM)		ED (VR)	CO (KM)	NT (PS)
SATURDAY	AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

CODE	SUBJECT	HOURS /WEEK	STAFF
18MAT31	Transform Calculus, Fourier Series and Numerical Techniques (M3)	5	Ms. Divya R. (DR)
18EC32	Network Theory (NT)	4	Mr. Puneeth S (PS)
18EC33	Electronic Devices (ED)	4	Mr. Vinay R (VR)
18EC34	Digital System Design (DSD)	4	Mrs. Renuka V Tali (RVT)
18EC35	Computer organisation and architecture (CO)	4	Dr. Kishore M (KM)
18EC36	Power Electronics Instrumentation (PEI)	4	Mr. Gopal KrishnaMurthy C R (CRG)
18ECL37	Electronic devices & Instrumentation Laboratory (EDL)	3	Mr. Gopal KrishnaMurthy C R (CRG)/ Mr. Syed Waseem Tabraiz (SWT)/ Mrs. Deepa R Bhangi (DRB)
18ECL38	Digital System Design Lab (DSD)	3	Mr. Sanjay B Nayak (SBN)/ Mr. Syed Waseem Tabraiz (SWT)/ Ms. Zaiba (ZA)
18CPC39/49	Constitution of India/Professional Ethics & Cyber Law (CPH)	1	Mrs. Anuradha M V (AMV)

  
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Class Teacher

Mr. Puneeth S

9164812059

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**ACADEMIC YEAR : 2019-20 (ODD SEMESTER)**  
**CLASS TIME TABLE**

CLASS : VECE 'A'

LECTURE HALL:209A

DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50
MONDAY	DSP (SB)	ITC (MDK)	TEA BREAK	VHDL (SGM)	PE-1 (KP/ZA)	LUNCH BREAK	DSPL (Batch-1/SB,AKM) / HDL (Batch-2/SS,KB)		
TUESDAY	M&E (GTJ)	ITC (MDK)		OE-1 (SBN/AKM)	VHDL (SGM)		DSP (SB)	TUTORIAL	LIBRARY
WEDNESDAY	OE-1 (SBN/AKM)	DSP (SB)		ITC (MDK)	M&E (GTJ)		DSPL (Batch-2/DND,AKM) / HDLL (Batch-3/SS,KM)		
THURSDAY	ITC (MDK)	PE-1 (KP/ZA)		M&E (GTJ)	OE-1 (SBN/AKM)		VHDL (SGM)	M&E (GTJ)	VHDL (SGM)
FRIDAY	PE-1 (KP/ZA)	OE-1 (SBN/AKM)		DSP (SB)	PE-1 (KP/ZA)		DSPL (Batch-3/SB,RBA) / HDLL (Batch-1/SS,KB)		
SATURDAY	AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

z	SUBJECT	HOURS /WEEK	STAFF
17ES51	Management and Entrepreneurship Development (M&E)	4	Mrs. Gunasheela T J (GTJ)
17EC52	Digital Signal Processing (DSP)	4	Mr. Senthil Babu (SB)
17EC53	Verilog HDL (VHDL)	4	Mrs. Shanthala G M (SGM)
17EC54	Information Theory and Coding (ITC)	4	Mr. Manu D K (MDK)
17EC55X	Professional Elective-1 (Operating System, Msp430) (PE-1)	4	Dr. Karthik P (KP) / Ms. Zaiba (ZA)
17EC56X	Open Elective -1 (Automotive Electronics, C++) (OE-1)	4	Mr. Sanjay B Nayak (SBN) / Mr. Arun Kumar M (AKM)
17ECL57	DSP Lab (DSPL)	3	Mr. Senthil Babu (SB) / Dr. Dushayanth N D (DND) / Mr. Arun Kumar (AK) / Mr. Ravikiran B A (RBA)
17ECL58	HDL Lab (HDLL)	3	Mrs. Shalini Shrivani (SS) / Mrs. Kripa (KB) / Dr. Kishore M (KM)

Class Teacher

Mrs.Zaiba

8138019458

Time Table Co-ordinator

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**ACADEMIC YEAR : 2019-20 (ODD SEMESTER)**

**CLASS TIME TABLE**

CLASS : V ECE 'B'

LECTURE HALL:209

DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50
MONDAY	DSPL (Batch-1/AKM,RBA) / HDLL (Batch-2/RVT,KM)				PE-1 (KP/ZA)	LUNCH BREAK	VHDL (RVT)	M&E (MSP)	ITC (MDK)
TUESDAY	VHDL (RVT)	DSP (SB)	TEA BREAK	OE-1 (SBN/AKM)	ITC (MDK)		DSPL (Batch-2/DND,RBA) / HDLL (Batch-3/RVT,KB)		
WEDNESDAY	OE-1 (SBN/AKM)	ITC (MDK)		M&E (MSP)	VHDL (RVT)		DSP (SB)	M&E (MSP)	TUTORIAL
THURSDAY	DSP (SB)	PE-1 (KP/ZA)		ITC (MDK)	OE-1 (SBN/AKM)		DSPL (Batch-3/SB,DND) / HDLL (Batch-1/RVT,KM)		
FRIDAY	PE-1 (KP/ZA)	OE-1 (SBN/AKM)		DSP (SB)	PE-1 (KP/ZA)		M&E (MSP)	VHDL (RVT)	LIBRARY
SATURDAY	AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

CODE	SUBJECT	HOURS /WEEK	STAFF
17ES51	Management and Entrepreneurship Development (M&E)	4	Mr. M Sasindran Prabhu (MSP)
17EC52	Digital Signal Processing (DSP)	4	Mr. Senthil Babu (SB)
17EC53	Venlog HDL (VHDL)	4	Mrs. Renuka V Tali (RVT)
17EC54	Information Theory and Coding (ITC)	4	Mr. Manu D K (MDK)
17EC55X	Professional Elective-1 (Operating System, Msp430) (PE-1)	4	Dr. Karthik P (KP) / Ms. Zaiba (ZA)
17EC56X	Open Elective - 1 (Automotive Electronics, C++) (OE-1)	4	Mr. Sanjay B Nayak (SBN) / Mr. Arun Kumar M (AKM)
17ECL57	DSP Lab (DSPL)	3	Dr. Dushyanth N D/Mr. Senthil Babu/ Mr. Arun Kumar/ Mr. Ravikiran B A
17ECL58	HDL Lab (HDLL)	3	Mrs. Renuka V Tali/Mrs. Kripa/ Dr. Kishore M

  
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 Bengaluru - 560 109

Class Teacher

Mrs.Senthil Babu

9886471877

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ACADEMIC YEAR : 2019-20 (ODD SEMESTER)  
CLASS TIME TABLE

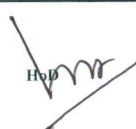
CLASS : VII ECE 'A'

LECTURE HALL:211

DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50
MONDAY	DIP (DND)	MA (PS)	TEA BREAK	DIP (DND)	PE (AKM)	LUNCH BREAK	ADCL (Batch-1/GTJ,PS) / VSLIL (Batch-2/SGM,DRB)		
TUESDAY	MA (PS)	PE-4 (KB)		DIP (DND)	PE-3 (RBA)		PE (AKM)	MA (PS)	LIBRARY
WEDNESDAY	PE-4 (KB)	PE-3 (RBA)		PE (AKM)	MA (PS)		ADCL (Batch-2/MDK,PS) / VSLIL (Batch-3/SAP,KB)		
THURSDAY	PE (AKM)	PE-4 (KB)		MA (PS)	PE-3 (RBA)		Project Work		
FRIDAY	PE-3 (RBA)	DIP (DND)		PE (AKM)	PE-4 (KB)		ADCL (Batch-3/MDK,GTJ) / VSLIL (Batch-1/SGM,SAP)		
SATURDAY	AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

CODE	SUBJECT	HOURS /WEEK	STAFF
15EC71	Microwave & Antenna (MA)	4	Mr. Puneeth S (PS)
15EC72	Digital Image Processing(DIP)	4	Dr. Dushayanth N D (DND)
15EC73	Power Electronics (PE)	4	Mr. Arun Kumar M (AKM)
15EC74X	Professional Elective-3 (Multimedia Communication) (PE-3)	4	Mr. Ravikiran B A (RBA)
15EC75X	Professional Elective-4 (Satellite Communication) (PE-4)	4	Mrs. Kripa (KB)
15ECL76	Advanced Communication Laboratory (ADCL)	4	Mr. Manu D K (MDK) / Mr. Puneeth S (PS) / Mrs. Gunasheela T J (GTJ)
15ECL77	VLSI Laboratory (VLSIL)	3	Mrs. Shanthala G M (SGM) / Mrs. Shilpa A P (SAP) / Mrs. Deepa (DRB) / Mrs. Kripa (KB)
15ECP78	Project Work Phase -1 +Project Work SEMINAR	3	Dr. Karthk P (KP) / Mr. M Sasindran Prabhu (MSP)

  
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Principal

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**ACADEMIC YEAR : 2019-20 (ODD SEMESTER)**  
**CLASS TIME TABLE**

CLASS : VII ECE 'B'

LECTURE HALL:313

DAY	8.40-9.35	9.35-10.30	10.30-10.45	10.45-11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50
MONDAY	ADCL (Batch-1/SS,ZB) / VLSIL (Batch-2/SAP,KB)				MA (GTJ)	LUNCH BREAK	DIP (RBA)	PE (VR)	LIBRARY
TUESDAY	PE (VR)	PE-2 (GVA)	TEA BREAK	MA (GTJ)	PE-1 (DND)		ADCL (Batch-2/SS,GTJ) / VSLIL (Batch-3/SGM,DRB)		
WEDNESDAY	PE-2 (GVA)	PE-1 (DND)		DIP (RBA)	PE (VR)		MA (GTJ)	DIP (RBA)	MA (GTJ)
THURSDAY	MA (GTJ)	PE-2 (GVA)		PE (VR)	PE-1 (DND)		ADCL (Batch-3/PS,MDK) / VSLIL (Batch-1/DRB,KB)		
FRIDAY	PE-1 (DND)	PE (VR)		DIP (RBA)	PE-2 (GVA)		Project Work		
SATURDAY	AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

CODE	SUBJECT	HOURS /WEEK	STAFF
15EC71	Microwave & Antenna (MA)	4	Mrs. Gunasheela T J (GTJ)
15EC72	Digital Image Processing (DIP)	4	Mr. Ravikiran B A (RBA)
15EC73	Power Electronics (PE)	4	Mr. Vinay R (VR)
15EC74X	Professional Elective -1 (Real Time Systems) (PE-1)	4	Dr. Dushyanth N D (DND)
15EC75X	Professional Elective -2 (Satellite Communication) (PE-2)	4	Dr. Girish V Attimarad (GVA)
15ECL76	Advanced Communication Laboratory (ADCL Lab)	4	Mr. Manu D K (MDK) / Mr. Puneeth S (PS) / Mrs. Gunasheela T J (GTJ)
15ECL77	VLSI Laboratory (VLSI Lab)	3	Mrs. Shanthala G M (SGM) / Mrs. Shilpa A P (SAP) / Mrs. Deepa Bhangi (DRB) / Mrs. Kripa K B (KB)
15ECP78	Project Work Phase -1 +Project Work Seminar	3	Dr. Karthk P (KP) / Mr. M Sasindran Prabhu (MSP)

  
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**FACULTY INDIVIDUAL TIME TABLE**

Faculty: Dr. Girish V Attimarad (GVA)

DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50	
MONDAY			TEA BREAK			LUNCH BREAK				
TUESDAY		E2(VIIB)								
WEDNESDAY	E2(VIIB)									
THURSDAY		E2(VIIB)						Project Work		
FRIDAY							E2(VIIB)	Project Work		
SATURDAY	AS PER CALENDAR OF EVENTS				AS PER CALENDAR OF EVENTS		AS PER CALENDAR OF EVENTS			

CODE	SUBJECT	HOURS/WEEK
15EC75X	Elective -2: Satellite Communication	4
18CSL38	Project work	1.5
	Project Seminar	2
	PhD Guidance	1

  
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**FACULTY INDIVIDUAL TIME TABLE**


Faculty: Dr. Karthik P (KP)

DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50	
MONDAY			TEA BREAK		PE-1 (VA)	LUNCH BREAK				
TUESDAY										
WEDNESDAY										
THURSDAY		PE-1 (VA)						Project Work		
FRIDAY	PE-1 (VA)						PE-1 (VA)	Project Work		
SATURDAY	AS PER CALENDAR OF EVENTS				AS PER CALENDAR OF EVENTS		AS PER CALENDAR OF EVENTS			

CODE	SUBJECT	HOURS/WEEK
17EC55X	Professional Elective-1 (Operating System)	4
15ECP78	Project Work Phase -1 +Project Work Seminar	1.5
	Project Seminar	2
	PhD Guidance	1

  
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**FACULTY INDIVIDUAL TIME TABLE**

Faculty: Dr. Dushyanth N D (DND)

DAY	8.40-9.35	9.35-10.30	10.30- 10.45	10.45 -11.40	11.40-12.35	12.35-1.20	1.20 -2.10	2.10-3.00	3.00-3.50	
MONDAY	DIP (VII A)		TEA BREAK	DIP (VII A)		LUNCH BREAK				
TUESDAY				DIP (VII A)	PE-3 (VII A)		DSPL (Batch-2) VB			
WEDNESDAY		PE-3 (VII A)					DSPL (Batch-2) VA			
THURSDAY					PE-3 (VII A)		DSPL (Batch-3) VB			
FRIDAY	PE-3 (VII A)	DIP (VII A)								
SATURDAY	AS PER CALENDAR OF EVENTS				AS PER CALENDAR OF EVENTS			AS PER CALENDAR OF EVENTS		

CODE	SUBJECT	HOURS/WEEK
15EC72	Digital Image Processing (DIP)	4
15EC74X	Professional Elective-3: Real Time System	4
17ECL57	DSP LAB	4.5
	PhD Guidance	1

  
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